

	Type	L #	Hits	Search Text	DBs
40	BRS	L40	8523	precharg\$3 and (multi\$3phase phase)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
41	BRS	L41	1111	40 and (domino dynamic) near4 logic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
42	BRS	L42	989	41 and clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
43	BRS	L43	916	42 and (transistor pmos nmos fet)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
44	BRS	L44	313	43 and 326/95-98.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB
45	BRS	L45	152	43 and "327"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB